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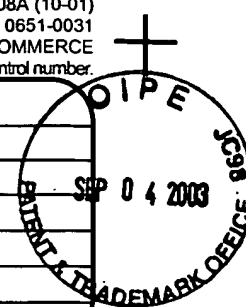
**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 1

**Complete if Known**

Application Number	09/608,624
Filing Date	June 30, 2000
First Named Inventor	JOURDAN et al
Group Art Unit	2183
Examiner Name	H. Tsai
Attorney Docket Number	2207/8609

**U.S. PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
115		Johnson, Mike, <i>Superscalar Microprocessor Design</i> , Chapter 10, pages 177-202, PTR Prentice-Hall, Englewood Cliffs, New Jersey, 1991	

Examiner Signature	<i>Henry Tsai</i>	Date Considered	10/8/03
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